

1 **In the Specification**

2 Please replace the text appearing on page 7, lines 15-17 with the following:

3
4 --Figs 31-37 graphically illustrate various matrix switch programming grid
5 states at select points in generating and configuring the matrix switch to
6 implement the media processing of Fig. 29.--

7
8 On page 7, please add the following text starting at line 25:

9
10 --Fig. 40 illustrates a bit map, in accordance with one described
11 embodiment.

12 Fig. 41 illustrates a bit map, in accordance with one described
13 embodiment.

14 Fig. 42 illustrates a bit map, in accordance with one described
15 embodiment.

16 Fig. 43 illustrates an exemplary bitmap processor, in accordance
17 with one described embodiment.

18 Fig. 44 is a flow diagram that describes steps in a method, in
19 accordance with one described embodiment.

20 Fig. 45 illustrates an example of how an exemplary bitmap processor
21 can be used to manipulate or otherwise operate upon an original bitmap, in
22 accordance with one described embodiment.

23 Fig. 46 illustrates an example of how an exemplary bitmap processor
24 can be used to manipulate or otherwise operate upon an original bitmap, in
25 accordance with one described embodiment.

1 Fig. 47 is a flow diagram that describes steps in a method, in
2 accordance with one described embodiment.

3 Fig. 48 shows an exemplary display having two videos that are being
4 processed, in accordance with one described embodiment.

5 Fig. 49 is a flow diagram that describes steps in a method, in
6 accordance with one described embodiment.--
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